

Book Catalog

Spring 2003

INTEL
PRESS

*Timely, technical, and
innovative books
to educate the computer industry*

intel®

INTRODUCTION



Welcome

Ever since I wrote a book on programming the Intel® 80386 processor in 1987, I have always wanted to be published in the august company of such Intel technology luminaries as Walter Triebel and John Hyde. Unfortunately, this has remained an unfulfilled ambition—and this introduction will probably come closest to the realization of this ambition for some time to come.

Why does Intel Press exist? Doesn't the technical press at large generate sufficient work to satisfy the needs of even the most ambitious developer? The short answer is NO. As the pace and depth of Intel's new technology introduction increases, it is difficult for the general technical press to keep up. In this case, the brains that are closest to the technologies—often its creators—must fulfill their responsibility of disseminating the information to the developer community. This is critical to Intel's success. We are “building block” suppliers and our success depends on the industry's informed and effective use of our building block technologies.

Since its inception in 1999, Intel Press has established an impressive portfolio. The breadth of topics—from PCI Express to the IT Best Practices Series—is a reflection of both Intel's diversity as well as our commitment to serve a broad technical community. I hope that you will take advantage of this resource to further both your technical education as well as the horizons for our industry as a whole.

Thank you,

A handwritten signature in black ink, which appears to read "P. Gelsinger". The signature is stylized with a large, looping initial 'P' and a long, sweeping underline.

Patrick Gelsinger
Senior Vice President & Chief Technology Officer
Intel Corporation

Intel Press publishes books written by engineers, for engineers. We have traveled the world talking to professionals like you, asking what new books they would like to read and use as reference materials. This catalog is a listing of current and upcoming publications in response to their answers. These timely, technical, and innovative books are designed to educate the computer industry and to help our readers adopt advancements in the Intel® architectures more quickly.

What's New

In this issue, we are presenting five new titles. For the computer system designer, *Introduction to PCI Express: A Hardware and Software Developer's Guide* by Adam H. Wilen, Justin P. Schade, and Ron Thornburg introduces the new serial I/O architecture of PCI Express.

For network engineers trying to optimize packet processing, *Intel® Internet Exchange Architecture and Applications: A Practical Guide to Intel's Network Processors* by Bill Carlson gives an invaluable field engineer's perspective.

For software engineers and programmers, *Change-Based Test Management: Improving the Software Validation Process* by Jon Sistowicz and Ray Arell shows how Intel maximizes efficiency of the software validation process.

IT managers will get the edge they need with *Building Operational Excellence: IT People and Process Best Practices* by Bruce Allen and Dale Kutnick.

We Value Your Feedback

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Each of our books is validated with its intended audience. We invite your participation. By joining our Customer Advisory program, you can get an early look at what's coming from Intel Press, help improve the value of our books by reviewing them and providing feedback, and receive a free copy of the finished book you help us with. You can apply to the Customer Advisor program by simply clicking *Register to receive news* and selecting *Yes* when asked whether you would like to become a Customer Advisor. We look forward to hearing from you!

Cbtr

PROGRAMMING

Make software validation a competitive advantage

Change-Based Test Management

Improving the Software Validation Process

Jon Sistowicz and Ray Arell

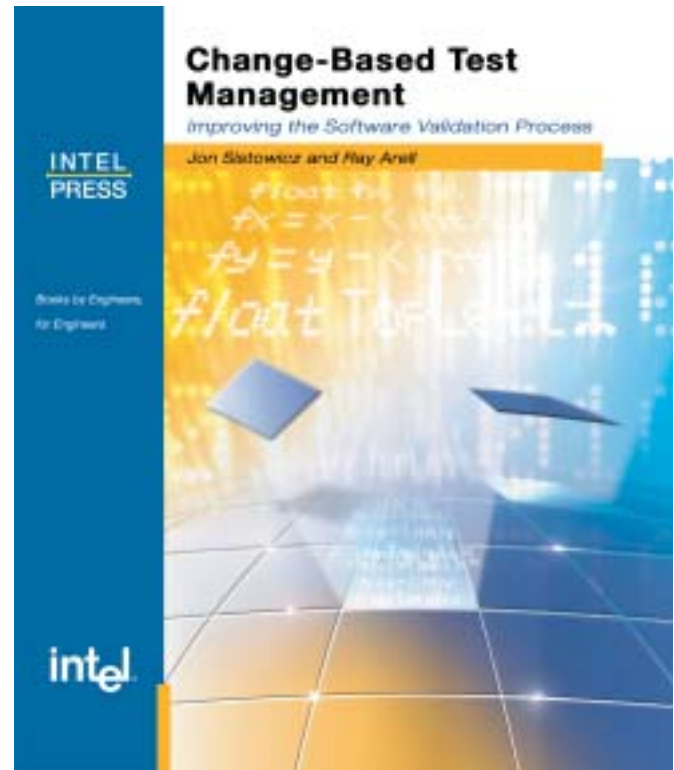
With the difficulties of today's economy, many companies are placing a renewed emphasis on reducing operational expenses. Unfortunately, software validation is often one of the first areas to feel the pressure of lower budgets. With the ever-increasing complexity and speed of today's hardware, how can you continue to produce a high quality product while maintaining or even lowering your operational costs? The answer lies in refining the validation process.

This book is an introduction to Change-based Test Management (CBTM), specifically written for software engineers and testing specialists. CBTM is a prioritization scheme that focuses on testing the changed portions of software first. Using this new methodology, software quality can be significantly improved, while development time can be reduced.

Topics include an overview of current development models and test methodologies, a complete and comprehensive study of CBTM, in-depth case studies, and test automation techniques. Learn how to:

- Use CBTM to increase software quality and decrease testing time and cost.
- Validate earlier in the development cycle to lower the cost of removing defects.
- Write better tests, monitor their performance, and measure the improvement.
- Automate the validation tasks using the right tools for the job.

Jon Sistowicz is a member of the Intel chipset software validation team where he helped develop and introduce CBTM. Jon created the first delta report generator, which established CBTM as a cornerstone of Intel's driver validation efforts. As part of Intel's chipset team, Jon implemented VHDL coverage practices and helped write a key feature specification for Intel's next generation graphics chipset. Prior to Intel, Jon worked for

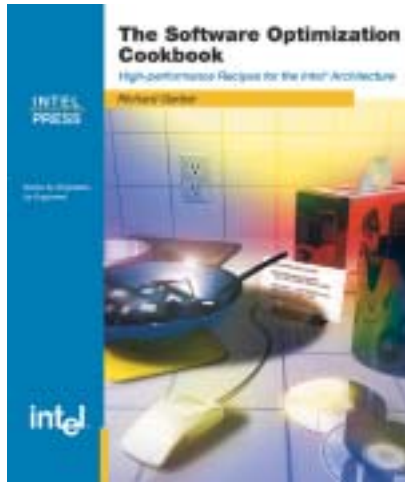


Unisys Corporation where he wrote Java applications enabling legacy hardware for the Internet. Jon holds a Bachelor's Degree in Computer Engineering from Pennsylvania State University.

Ray Arell manages the Validation Architecture department within the Intel Desktop Platforms Group. Ray has over 18 years of development, validation, and management experience, and has been with Intel for 10 years. During this time, he has worked on a variety of teams focused on CPU, chipsets, and graphics system-level testing including the i386™, i486™, Pentium® processors, and supporting chipsets. Ray's programming projects have included a variety of test application software including the concept and development of key validation capabilities like the graphics Visual Test Creator.

186 PAGES, PAPER
U.S. \$49.95 (CAN \$74.95) **0-9717861-2-7**

PROGRAMMING



How to write efficient programs for Intel® Pentium® processor family

The Software Optimization Cookbook

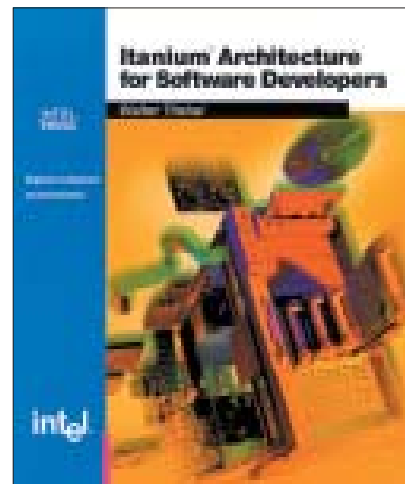
High-Performance Recipes for the Intel® Architecture

Richard Gerber

Revealing the secrets of the software optimization process, this guide provides recipes for high-performance applications on the Intel® Pentium® III and Pentium 4 processors. Simple explanations and C/C++ examples show you how to address performance issues with algorithms, memory access, branching, SIMD instructions, multiple threads, and floating-point optimizations. With this book you can learn to use performance tools to improve applications, to identify the reason that portions of an application are slower than they should be, and to design an application from the ground up.

Richard Gerber has worked on multimedia projects, 3D libraries, and computer games for Intel. As a software engineer, he works on the Intel® VTune™ Performance Analyzer and leads training sessions on optimization techniques.

280 PAGES, INCLUDES CD-ROM, PAPER
U.S. \$49.95 (CAN \$74.95) **O-9712887-1-2**



How to adapt software to the Itanium® architecture

Itanium® Architecture for Software Developers

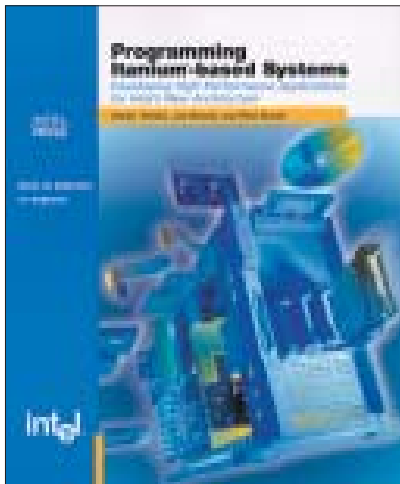
Walter Triebel

Foreword by James Reinders

This unique description of the Itanium®-based application architecture helps the software development community use the power of Intel's new line of Itanium® processors. Developers learn how to create high-performance software by tapping the unique features of the architecture and corresponding compilers. This book clearly explains how the massively parallel Itanium architecture meets demand for increased throughput by Internet-based applications and e-commerce. Also included are descriptions of salient features of the Itanium processor's application software architecture and of unique ways that the Itanium architecture differs from traditional architectures.

Walter Triebel is the author of 17 books on microprocessors, digital logic, and memory including *The 8088 and 8086 Microprocessors*. He teaches computer architecture at Farleigh Dickinson University.

308 PAGES, PAPER
U.S. \$69.95 (CAN \$104.95) **O-9702846-4-0**



A programming guide for software application developers targeting the Itanium® processor family

Programming Itanium®-based Systems

Developing High Performance Applications for Intel's New Architecture

Walter Triebel, Joe Bissell, and Rick Booth

Helping developers harness the power of Intel's new line of very long instruction word (VLIW) processors, this guide provides insight into the effective use of Itanium®-based application software development tools. Beginning with code optimization advice, including an explanation of the process of code compilation, this resource tells programmers how to take advantage of the EPIC architecture's parallelism. A thorough treatment of porting applications to the Itanium-based systems environment is provided along with unique insights into optimization and tuning of Itanium-based applications.

Walter Triebel is the author of *Itanium® Architecture for Software Developers* and is currently an adjunct professor at Fairleigh Dickinson University.

Joe Bissell teaches computer architecture at the University of Delaware.

Rick Booth manages digital video production and delivery at Visible World and is the author of *Inner Loops*.

360 PAGES, PAPER
U.S. \$64.95 (CAN \$98.95) **0-9702846-2-4**



How to support high-precision computations on computers using Intel® Itanium® architecture

Scientific Computing for Itanium®-based Systems

Marius Cornea, John Harrison and Ping Tak Peter Tang

Covering key and unique architectural features of the Intel® Itanium® processor that make it the platform of choice for high-quality numerical software, this reference provides examples of typical floating-point computations presented in assembly and/or high-level languages. The book discusses run-time support for scientific and engineering computation and support for high-precision results. It explains the selection of kernel libraries for numerical calculations and the role of language tools available on Itanium-based platforms for C/C++ and Fortran 90. Also covered is porting scientific and engineering software from other platforms to those based on Itanium processors.

Marius Cornea has designed and implemented various floating-point algorithms and exception handlers for the Intel Itanium and IA-32 architectures.

John Harrison is involved in the design and formal validation of numerical algorithms.

Ping Tak Peter Tang is a key algorithm designer for the run-time library for Itanium processors.

406 PAGES, PAPER
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COMPUTER SYSTEM DESIGN

Architecting computers for the latest I/O technology

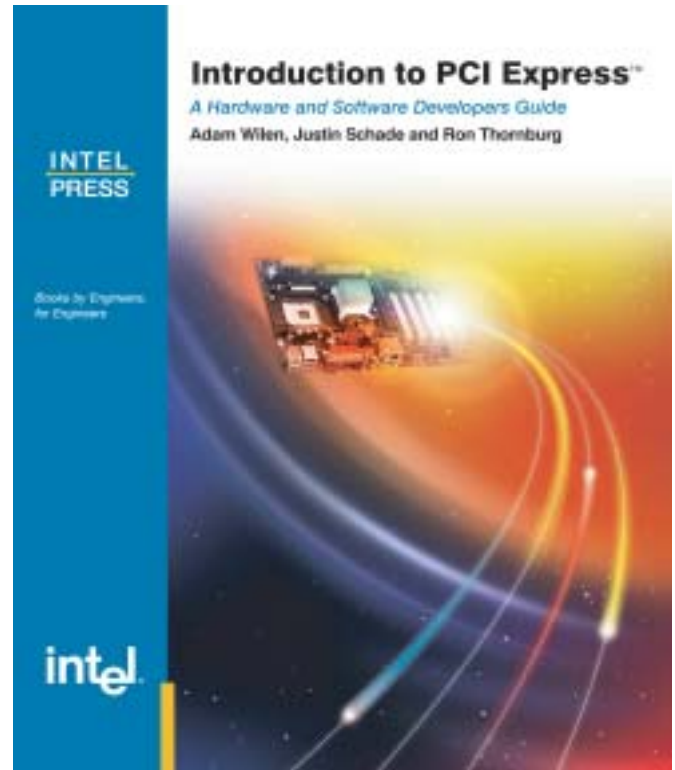
Introduction to PCI Express[†]: A Hardware and Software Developer's Guide

**Adam H. Wilen, Justin P. Schade,
and Ron Thornburg**

This book offers an introduction to the new I/O technology, PCI Express, designed to allow increasing levels of computer system performance. The book details how PCI Express technology allows designers to overcome the practical performance limits of existing multi-drop, parallel bus technology. It explains how to increase performance and add new capabilities for a broad range of computing and communications platforms. Relevant technical considerations are presented that both hardware and software developers need to comprehend in order to employ PCI Express technology in next generation systems.

Adam H. Wilen is a platform applications engineer at Intel, serving as a key point of contact for customers and technical field sales. Adam's expertise in PCI Express technology comes from his involvement in implementing Intel's first generation work on PCI Express chipsets. Prior to his work on PCI Express technology, he developed graphics controllers and chipsets, with a specialization in graphics and display technologies.

Justin P. Schade is an applications engineer for Intel, who interfaces directly with OEM customers wanting to take advantage of PCI Express technology. He has worked on a variety of next generation I/O controllers and specialized in the design-in support associated with each controller. He is currently working on enabling Super I/O hardware vendors to take advantage of PCI Express technology transition.



Ron Thornburg is a technology initiatives manager for Intel, currently working directly with customers on the transition to PCI Express technology. Ron has performed a variety of roles within Intel, including chipset applications support and product marketing for high density Ethernet controllers.

300 PAGES, PAPER
U.S. \$69.95 (CAN \$104.95) **O-9702846-9-1**

AVAILABLE APRIL 2003



A hands-on guide to designing and implementing USB devices

USB Design by Example

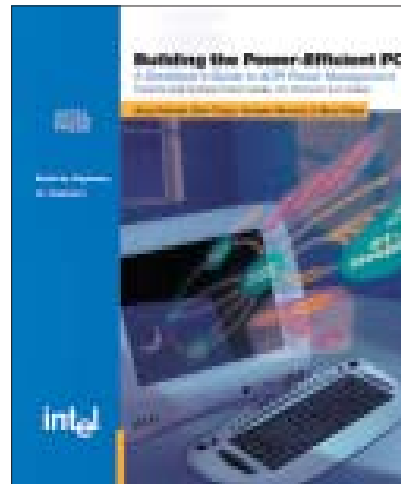
*A Practical Guide to Building I/O Devices
Second Edition*

John Hyde

This unique guide goes beyond all the Universal Serial Bus (USB) specification overviews to provide designers with the expert knowledge and skills they need to design and implement USB I/O devices. Organized around a series of fully documented, real-world examples, this book is structured to serve as both a step-by-step guide for creating specific devices and as a complete reference to USB. Design examples cover most USB classes—HID, communications, audio, mass-storage, and hub—and provide insights into high-speed USB 2.0 devices, including a device driver for a vendor class called blockio.

John Hyde was the editor of the *PC98 System Design Guide*. He has been responsible for providing technical documentation, demonstrations, and technical training for a wide range of Intel products.

510 PAGES, INCLUDES CD-ROM, PAPER
U.S. \$54.95 (CAN \$86.95) **0-9702846-5-9**



A complete reference for engineering power-managed personal computers

Building the Power-Efficient PC

A Developer's Guide to ACPI Power Management

Jerzy Kolinski, Ram Chary, Andrew Henroid, and Barry Press

Foreword by Craig Hershberg, product manager, ENERGY STAR[†], United States Environmental Protection Agency

A comprehensive hardware and software engineering guide for designing power-managed PCs, this book provides developers and integrators with practical knowledge and design techniques for building PCs that address the increasing demand for energy conservation. The companion CD includes sample code, complete power management documentation, Intel[®] power management tools, and links to references.

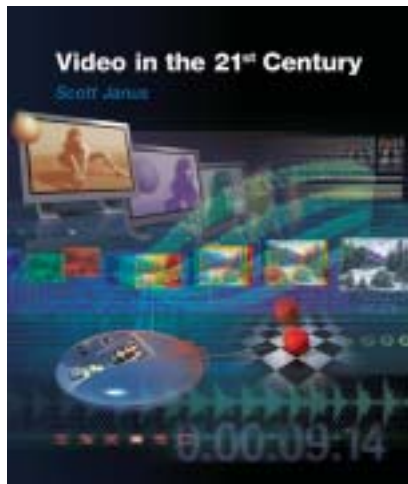
Jerzy Kolinski developed the first desktop PC to showcase new ACPI power management concepts and has helped OEMs to deliver Instantly Available PC (IAPC) products.

Ram Chary is a systems architect in the Intel Platform Architecture Lab who specializes in the software aspects of enabling IAPC.

Andrew Henroid led the early development of ACPI and power management for Linux[†] and has contributed to a wide variety of projects within the open-source community.

Barry Press is the author of *PC Upgrade and Repair Bible*, *Networking by Example*, and *Teach Yourself PCs*.

249 PAGES, INCLUDES CD-ROM, PAPER
U.S. \$49.95 (CAN \$74.95) **0-9702846-8-3**



A comprehensive reference for the digital video engineer

Video in the 21st Century

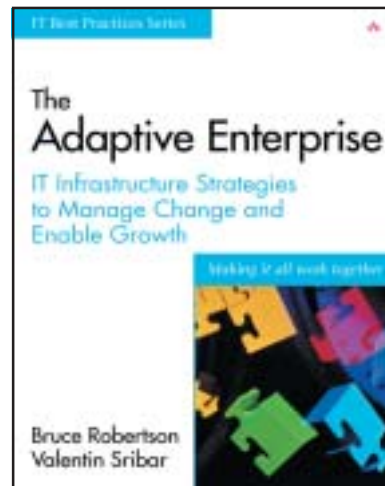
Scott Janus

Filled with useful video information, this book benefits hardware and software designers who create digital multimedia products. In the past few years, video technology has made huge advancements that are putting professional-grade video into the hands of consumers.

Video in the 21st Century explores the key technologies that have made all of this possible. Starting with basic video concepts, this book provides an easy-to-read tutorial for novices, and serves as a useful reference for experienced engineers.

Scott Janus has worked as a professional videographer and helped develop a nonlinear video editing system. He has been a senior engineer on numerous hardware and software MPEG projects and currently contributes to the development and launch of Intel's graphics chipsets.

340 PAGES, INCLUDES CD-ROM, CLOTH
U.S. \$54.95 (CAN \$82.95) **O-9712887-5-5**



A guide to developing flexibility in your business systems

The Adaptive Enterprise

IT Infrastructure Strategies to Manage Change and Enable Growth

Bruce Robertson and Valentin Sribar

The strategies and processes described in this book give you clear and practical ways to guide your company through Internet-induced change. A number of successful models show you how to design and implement an adaptive infrastructure, identifying key infrastructure patterns within your organization and using those patterns to derive reusable services. You learn to make great technology choices and to develop the people and processes that bring the success that you expect.

Bruce Robertson leads Meta Group's infrastructure development program, working with clients to plan their e-Business and enterprise systems from a pattern perspective. His areas of technical specialization include directory and security services, networking, middleware, and e-Business infrastructure.

Valentin Sribar is general manager of Meta Group's Infusion offerings, helping clients adapt their processes and organization to cope with any combination of dramatic business and technology changes.

299 PAGES, CLOTH
U.S. \$39.99 (CAN \$59.95) **O-2017673-6-8**

Your secret weapon in the fight to drive synergy and performance

Building Operational Excellence

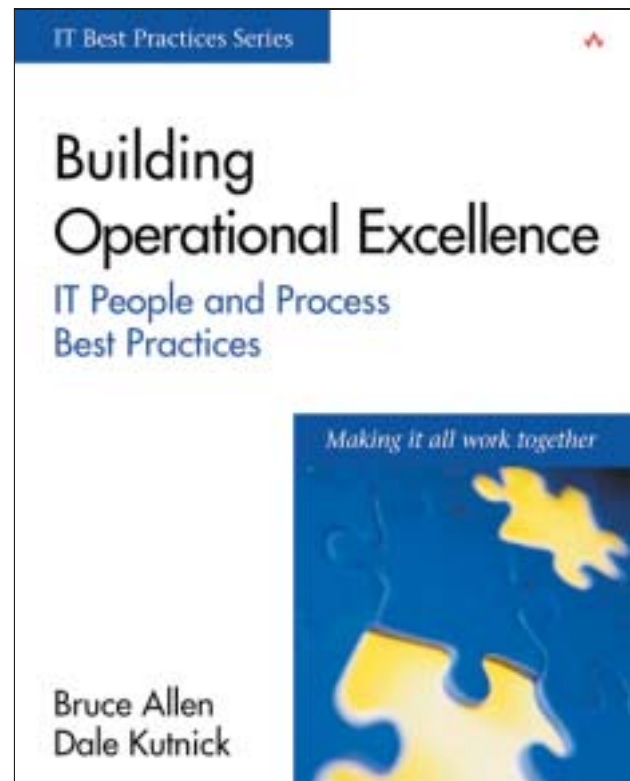
IT People and Process Best Practices

Bruce Allen, Dale Kutnick

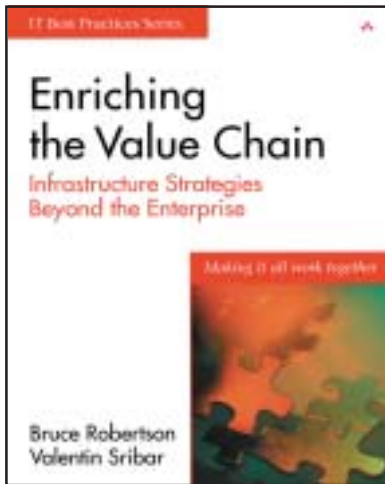
Running an effective and efficient IT organization goes beyond just having the right technology in place. IT organizations must have effective ways to meet increased workloads, manage staff levels, and to collaborate more effectively with business units. Building Operational Excellence provides valuable insight for organizing IT people and processes, showing you how to improve end-to-end management of critical resources. This book guides you through techniques of analysis, assessment, and change management that help create the center of excellence. It also offers techniques for implementing meaningful metrics to drive and demonstrate the business value of IT. Although you can find many reports and briefs on the topics of infrastructure and operations excellence, this book provides a single source of industry-approved, affordable information.

Bruce Allen is a vice president of META Group. Before joining META in August 1990, he managed corporate central processing of hardware and software resources at the Hartford Insurance Group, where he was responsible for procurement and strategic planning. During his seventeen-year data-processing career, he has been involved in all aspects of central environment support, including operations, systems programming, performance analysis, rates/chargeback, budgeting, service levels, forecasts, and overall planning.

Dale Kutnick is the cofounder, CEO, and chairman of the board of META group, overseeing all of the company's research and analytical activities. Prior to cofounding META Group in 1989, Mr. Kutnick was executive vice president of research at Gartner Group. Previously, he was executive director and a principal at Yankee Group, and a principal at Battery Ventures, a venture capital firm.



300 PAGES, CLOTH
U.S. \$39.95 (CAN \$59.95) 0-2017673-7-6



A guide to developing flexible and reusable e-Business applications

Enriching the Value Chain

Infrastructure Strategies Beyond the Enterprise

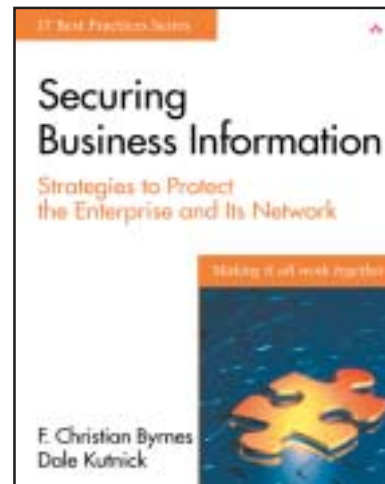
Bruce Robertson and Valentin Sribar

How do forward-thinking IT professionals organize infrastructure and staff to meet the challenges of e-Business value chain initiatives head-on? *Enriching the Value Chain* gives you the answers, providing strategic advice for mapping business drivers to a set of fundamental patterns and planning models. It clearly explains a way to organize e-Business infrastructure with your suppliers, trading partners, and customers. Picking up where *The Adaptive Enterprise* leaves off, this companion volume tells you how to achieve success in value chain infrastructure planning.

Bruce Robertson leads Meta Group's infrastructure development program, working with clients to plan their e-Business and enterprise systems from a pattern perspective. His areas of technical specialization include directory and security services, networking, middleware, and e-Business infrastructure.

Valentin Sribar is general manager of Meta Group's Infusion offerings, helping clients adapt their processes and organization to cope with any combination of dramatic business and technology changes.

370 PAGES, CLOTH
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How to tailor enterprise security techniques to your business

Securing Business Information

Strategies to Protect the Enterprise and Its Network

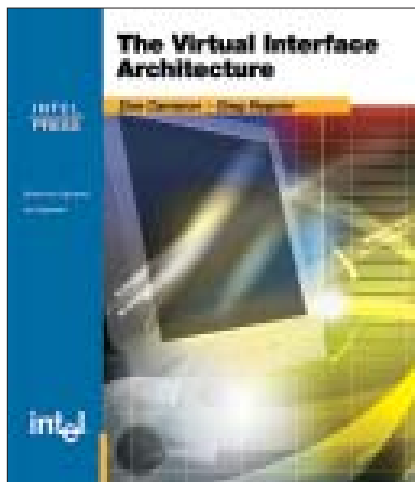
F. Christian Byrnes and Dale Kutnick

This approach to security is derived from numerous successful implementations. The Enterprise Security Plan (ESP) is a six-step process for tailoring enterprise security techniques to the needs of your business. This book guides you through these steps to secure your computing infrastructure within the constraints of normal business operations, resources, and today's technology. With this book, you can plan the projects to implement an appropriately secure enterprise.

F. Christian Byrnes leads Meta Group's security coverage. He is the author of *Security in Enterprise Computing: A Practical Guide*.

Dale Kutnick is the cofounder, CEO, and Chairman of the Board of Meta Group, overseeing all of the company's research and analytic activities.

237 PAGES, CLOTH
U.S. \$39.99 (CAN \$59.95) **0-2017673-5-X**



How to develop software that interfaces a computer to a high-speed switched network

The Virtual Interface Architecture

Don Cameron and Greg Regnier

Providing an overview of the motivation, benefits, and history of the Virtual Interface Architecture as well as a guide to the syntax and semantics of the VI Provider Library API, this reference helps hardware and software engineers use the VI Architecture to develop scalable, high-performance, and fault-tolerant systems. The authors explain how a new interface solves the long-standing problem of efficiently interfacing general-purpose computers to high-speed switched networks, just as Virtual Memory allowed personal computers to break through physical memory limitations. The book includes examples demonstrating the different ways to use the API.

Don Cameron is a senior staff architect in the Intel Architecture Lab and has worked on enterprise server storage architecture and the Paragon and Tflops supercomputers.

Greg Regnier is currently a principal engineer in the Intel Architecture Lab. He has been a developer of massively parallel supercomputers and real-time media servers.

203 PAGES, PAPER

U.S. \$44.95 (CAN \$67.95) **0-9712887-0-4**



How the new InfiniBand[®] architecture delivers industry-leading performance through innovative features

InfiniBand[†] Architecture Development and Deployment

A Strategic Guide to Server I/O Solutions

William T. Futral

The first to interpret the InfiniBand specification as it applies to high-performance, inter-system I/O, this book provides the knowledge and insight necessary to develop innovative solutions for system I/O and inter-process communication. Developers are guided through the architecture and shown how to implement and deploy InfiniBand-based products to relieve I/O bottlenecks. Also discussed are deployment strategies, InfiniBand-based applications, and management.

William T. Futral is an I/O architect on the Intel Advanced Components Division Architecture Team. As a veteran of standards activities, he was the author of various specifications, in particular the IEEE 802.5 token ring and intelligent I/O architecture. Currently, he serves as co-chair of the InfiniBand Trade Association's Application Working Group.

349 PAGES, PAPER

U.S. \$64.95 (CAN \$98.95) **0-9702846-6-7**

NETWORKING

How to build application-optimized systems with Intel's network processors

Intel® Internet Exchange Architecture and Applications

A Practical Guide to Intel's IXP2XXX Network Processors

Bill Carlson

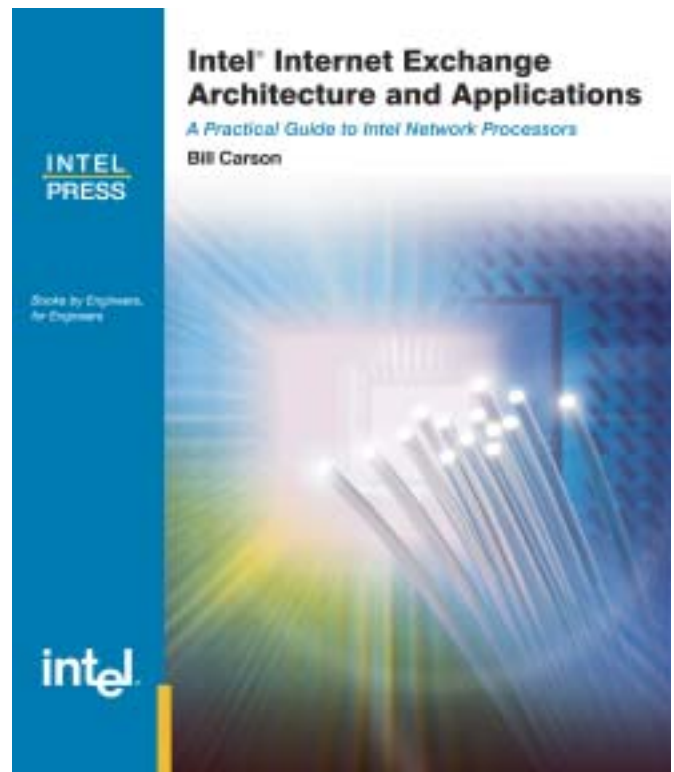
This invaluable developer resource provides an overview of Intel Internet Exchange Architecture (IXA), educates developers about Intel network processors, and provides an in-depth technical view of the standards required by hardware and software developers of next-generation OEM networking equipment. The text is well suited not only for hardware and software engineers but also for users outside the industry, such as support professionals, management, sales, and marketing. The architecture of a typical network core or edge is described to provide a context for the network processor architecture. This guide also explores the architecture of the Intel® IXP2400 and Intel® IXP2800 network processors and provides a detailed example of a DSLAM using the multiprotocol software framework.

Bill Carlson has over 17 years experience in communications. In his current position, he is an Intel Network Processor Specialist and a Field Applications Engineer. Bill provides active applications support of the IXP1200 Network Processor from product inception through present third-generation processors. He has a BSEE Electrical and Computer Engineering degree from the University of Michigan.

250 PAGES, PAPER

U.S. \$64.95 (CAN \$97.95) **0-9702846-3-2**

AVAILABLE APRIL 2003





A guide to sharing resources at home and in the corporate world

Peer-to-Peer Computing

Technologies for Sharing and Collaborating on the Net

David Barkai

Foreword by Patrick Gelsinger, Chief Technology Officer at Intel Corporation

This insider's account of Peer-to-Peer (P2P) computing takes readers from past experimental projects to the present resurgence and looks into its future as a computing model for business and consumers on the Internet. Written for application developers, IT professionals, and end users, this book reveals what the P2P buzz is all about. Included are some early success stories and discussions of the challenges still ahead. The author explains technologies that form the foundation on which P2P applications can be developed, along with solutions and innovations from the pioneering work done by the most creative developers in the P2P arena. This guide portrays P2P computing as a viable set of technologies and a computing model for business and the enterprise, as well as for self-managed online communities of consumers.

David Barkai is a member of the Peer-to-Peer Architecture group of Intel Labs. He has also been a content architect for the Intel Developer Forum conference and a software scientist in the Microcomputer Software Lab.

346 PAGES, CLOTH
U.S. \$39.95 (CAN \$59.95) **0-9702846-7-5**



How to program Intel® IXP1200 Network Processors

IXP1200 Programming

The Microengine Coding Guide for the Intel® IXP1200 Network Processor

Erik J. Johnson and Aaron R. Kunze

This book shows software engineers how to program the *microengines* of Intel's IXP1200 family of network processors. Assuming no particular background or experience with the IXP1200 architecture, the book begins with an explanation of the hardware and development environment. You get an explanation of the right, and sometimes the wrong, approaches to programming the microengines through a series of expanding examples written in structured microC "microblocks," with microengine assembly code used occasionally for illustration.

Erik J. Johnson has worked on embedded systems and has programmed IXP12xx microengines for Intel.

Aaron R. Kunze is a senior network software engineer in the Corporate Technology Group at Intel.

322 PAGES, INCLUDES CD-ROM, PAPER
U.S. \$49.95 (CAN \$74.95) **0-9712887-8-X**



A guide to network technology for the home or small office

Building a Simple Network

*How to Set Up a Small Network of Personal Computers
Second Edition*

Ken Denniston

This practical explanation of standard ethernet networking products tells how to connect a group of computers, printers, and communications devices in a small business or home office. Covering installation, set-up, and use of a network based on Microsoft Windows[®] XP, Windows 2000, Windows Me, Windows 98, and Windows 95 operating systems, users will need only moderate experience to become the de facto computer expert in the office. This clearly written guide discusses what hardware to buy, provides steps for installing the hardware and software, offers tips on using the network effectively, and helps troubleshoot networking problems. Also provided is information on new technology for expanding the network as needs grow.

Ken Denniston is an information engineer at Intel in the networking products division. He has been the lead information engineer for the Intel[®] NetportExpress[™] print server products and designed the Intel[®] InBusiness[™] Network Setup Wizard.

271 PAGES, PAPER

U.S. \$29.95 (CAN \$44.95) **0-9702887-6-3**

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Order Number: A91500-003

